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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

2

Complete If Known

Application Number

10127170

Filing Date

12/01/2003

First Named Inventor

Madurawe, Raminda U.

Group Art Unit

2879

Examiner Name

Attorney Docket Number

### U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
he		4,609,986		Hartmann et al.	09-02-1986	
		4,706,216		Carter	11-10-1987	
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		6,613,611		How et al.	09-02-2003	

### FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	10/27/70
		Filing Date	12/01/2003
		First Named Inventor	Madurawe, Raminda U.
		Group Art Unit	2519
		Examiner Name	
		Attorney Docket Number	
Sheet	2	of	2

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
he		SEALS & WHAPSHOTT, "Programmable Logic – PLDs and FPGAs", 1997, pp 102-117, McGraw-Hill, USA (NO MONTH) re	<input type="checkbox"/>
he		ASHOK K. SHARMA, "Programmable Logic Handbook – PLDs, CPLDs, & FPGAs", 1998, pp 99-171, McGraw-Hill, USA (NO MONTH) re	<input type="checkbox"/>
he		V. BETZ, J. ROSE, A. MARQUARDT, "Architecture and CAD for Deep-Submicron FPGAs", Feb 1999, Kluwer Academic Publishers, Boston (NO MONTH) re	<input type="checkbox"/>
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